

Notice of Allowability

Application No.

10/728,536

Examiner

Truc T. T. Nguyen

Applicant(s)

KORSUNSKY ET AL.

Art Unit

2833

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to an interview on 11/11/04.
2. ☒ The allowed claim(s) is/are 1-5, 7-11 and 13-16.
3. ☒ The drawings filed on 12/05/03 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.


Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 12/05/03
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413),
Paper No./Mail Date 11/11/04
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____


TRUC T. NGUYEN
PRIMARY EXAMINER

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Wei Te Chung on November 11, 2004.

The application has been amended as follows:

- Please cancel claims 6, 12, and 17-20.
- Please amend claim 1 as follow:

1. An electrical connector comprising;

a unitary insulative housing defining a plurality of parallel slots;

a plurality of parallelly arranged circuit board modules each comprising a dielectric spacer, a circuit board attached to the dielectric spacer and received in a corresponding slot of the housing, and a row of contacts mechanically and electrically connecting with the circuit board and disposed in the corresponding slot; and

a shield member attached to the housing, the circuit board modules being retained by and between the shield member and the housing[.] ;

an alignment pin;

wherein the dielectric spacer of each circuit board module includes a recess and a plurality of laterally extending posts in the recess, and the circuit board is received in the recess of the dielectric spacer and defines a corresponding number of holes receiving the posts;

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wherein each of the circuit boards defines a through hole, and each of the dielectric spacers defines an opening aligned with the through hole, the alignment pin inserting through the housing, the through holes of the circuit boards and the openings of the dielectric spacers.

2. The following is an examiner's statement of reasons for allowance:

Regarding claims 1-5, 7-11 and 13, the prior art of record fails to teach the dielectric spacer of each circuit board module includes a recess and a plurality of laterally extending posts in the recess, and the circuit board is received in the recess of the dielectric spacer and defines a corresponding number of holes receiving the posts. The prior art of record also fails to teach the alignment pin inserting through the housing, the through holes of the circuit boards and the openings of the dielectric spacers.

Regarding claims 14-16, the prior art of record fails to teach the connecting portion having a projection received in the plated through hole, interstitial space between the plated through hole and the projection being substantially filled with fusible element after melting and solidifying the fusible element.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Truc T. T. Nguyen whose telephone number is 571-272-2011. The examiner can normally be reached on 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paula Bradley can be reached on 571-272-2800 extension 33. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Truc T. T. Nguyen
Primary Examiner
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